



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/679,803	10/06/2003	Douglas W. Constable	86339RRS	3339

7590

04/09/2004

Milton S. Sales
Patent Legal Staff
Eastman Kodak Company
343 State Street
Rochester, NY 14650-2201

EXAMINER

PERKEY, WILLIAM B

ART UNIT

PAPER NUMBER

2851

DATE MAILED: 04/09/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/679,803	Applicant(s) CONSTABLE, DOUGLAS W.	
	Examiner William B. Perkey	Art Unit 2851	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-13 and 18-20 is/are allowed.
- 6) ☐ Claim(s) _____ is/are rejected.
- 7) ☒ Claim(s) 14-17 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 06 October 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>10-06-03</u> . | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Drawings

1. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the thyristor of claim 10; the testing circuit of claim 19; the sensing of conditions indicating that the reset has taken place, sensing for conditions indicating whether the timing period has terminated, applying a short across the timing capacitor, charging the timing capacitor and causing the timing capacitor to discharge at fast rate, all performed by the testing circuit in claim 20 must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

Disclosure and Claims

2. Claims 14-17 are objected to because of the following informalities: Claim 14 and page 5 lines 23 and 24 are inconsistent with each other. Page 5 lines 23 and 24 indicate that the thyristor arrangement is an alternative to the latch circuit which uses two transistors whereas claim 14 sets forth that the two transistors are a thyristor. The examiner suggests that claims 14, 16, and 17 be canceled. To call a pair of latching transistors a thyristor is repugnant to the standard use of the term thyristor in the field of electronics. Claim 15 is objected to because there is no antecedent basis in the claim for "the detector". It appears that claim 15 should be amended to depend from claim 11, since a detecting circuit is set forth in claim 11.

Appropriate correction is required.

Allowable Subject Matter

3. Claims 1-13, 18-20 are allowed. The primary reason for allowance of claims 1-13 is the claim limitation that the reset circuit first applies battery voltage to the timing capacitor and then applies voltage generated by the voltage conversion circuit to the timing capacitor. The primary reason for allowance of claim 18 is the claim limitation for a diode connected to more than one transistor to suppress any voltage spikes that appear during oscillation at the transistors. The primary reason for allowance of claims 19 and 20 is the claim limitation for testing points connected across the timing capacitor.

Telephone Numbers

Any inquiry concerning this communication or earlier communications from the examiner should be directed to William B. Perkey whose telephone number is (571) 272-2126. The examiner can normally be reached on Monday-Thursday 7:00am-5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Russ Adams can be reached on (571) 272-2112. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



William B. Perkey

Application/Control Number: 10/679,803

Page 4

Art Unit: 2851

Primary Examiner
Art Unit 2851

WBP:wbp